

DMN53D0U

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	I _D T _A = +25°C
E0)/	2Ω @ V _{GS} = 5V	300 mA
50V	2.5Ω @ V _{GS} = 2.5V	200 mA

This MOSFET has been designed to minimize the on-state resistance

(R_{DS(ON)}) and yet maintain superior switching performance, making it

Features and Benefits

- N-Channel MOSFET •
- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23 .
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (approximate)

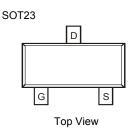


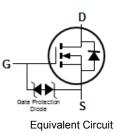
Description and Applications

ideal for high efficiency power management applications.



Top View





Ordering Information (Note 4)

Part Number	Case	Packaging
DMN53D0U-7	SOT23	3000/Tape & Reel
DMN53D0U-13	SOT23	10000/Tape & Reel

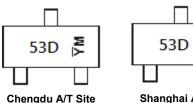
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



53D = Product Type Marking Code YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: B = 2014)

Shanghai A/T Site

Σ

M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2014	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D	E		F		G		Н
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain Source Voltage		V _{DSS}	50	V
Gate-Source Voltage	Continuous	V _{GSS}	±12	V
Drain Current (Note 5)	Continuous Pulsed	I _D I _{DM}	300 500	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	520	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ ext{ heta}JA}$	246	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

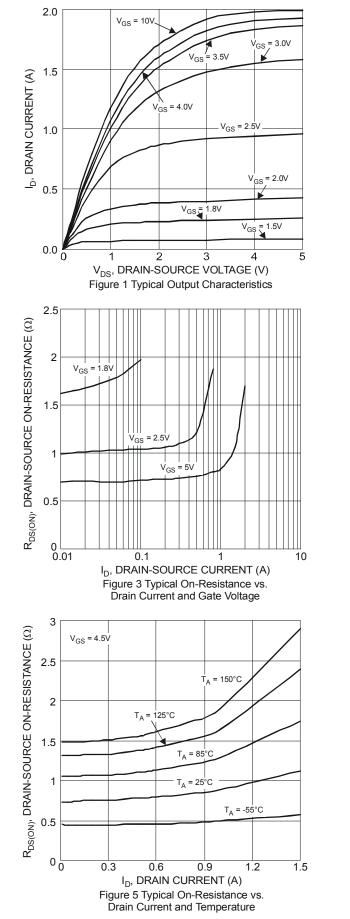
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

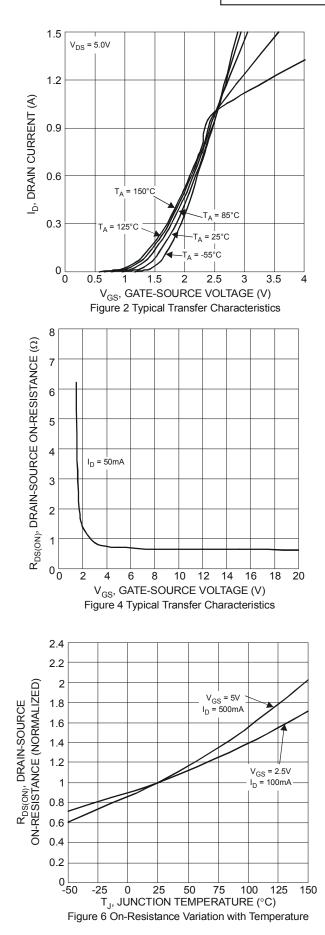
			Г <u>–</u>				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)	-						
Drain-Source Breakdown Voltage	BV _{DSS}	50		—	V	V_{GS} = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current	I _{DSS}	—		1	μA	V_{DS} = 50V, V_{GS} = 0V	
Gate-Body Leakage	I _{GSS}	_	_	±10	μA	V_{GS} = ±12V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	0.4		1.0	V	V_{DS} = V_{GS} , I_D = 250 μ A	
				2.0		V _{GS} = 5.0V, I _D = 50mA	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	2.5	Ω	V_{GS} = 2.5V, I_{D} = 50mA	
	. ,	_		3.0		V_{GS} = 1.8V, I_{D} = 50mA	
Source-Drain Diode Forward Voltage	V _{SD}	_		1.4	V	V _{GS} = 0V, I _S =115mA	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		37.1		pF		
Output Capacitance	C _{oss}	_	8.4	_	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	4.0	_	pF		
Total Gate Charge	Qg	_	0.6	—	nC		
Gate-Source Charge	Q _{gs}		0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_D = 250mA$	
Gate-Drain Charge	Q_{gd}		0.1		nC		
Turn-On Delay Time	t _{D(on)}		2.1	—	ns		
Turn-On Rise Time	tr	—	2.8	—	ns	V _{DD} = 30V, V _{GS} = 10V,	
Turn-Off Delay Time	t _{D(off)}		21		ns	$R_{G} = 25\Omega, I_{D} = 200 \text{mA}$	
Turn-Off Fall Time	t _f	_	14	—	ns		

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate

Bertee mounted of the substate to board, 202 copper, with
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.





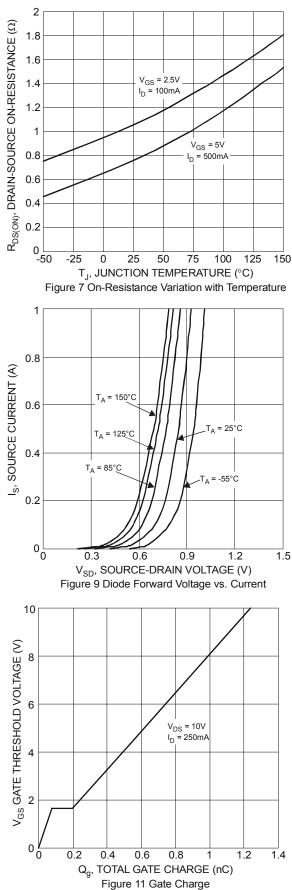


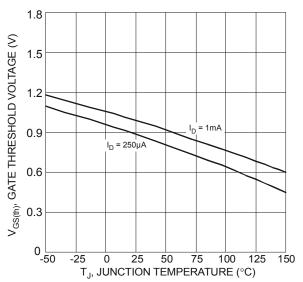
NEW PRODUCT

DMN53D0U Document number: DS37098 Rev. 2 - 2

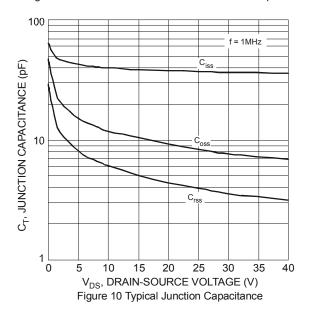


NEW PRODUCT





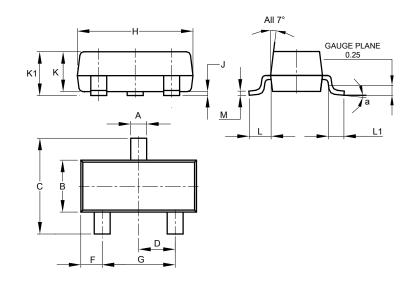






Package Outline Dimensions

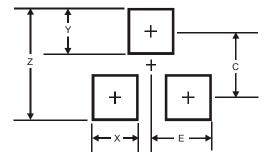
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
ĸ	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
Μ	0.085	0.150	0.110					
α	8°							
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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