

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at www.onsemi.com

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, emplo



June 2014

FDMA430NZ

Single N-Channel 2.5V Specified PowerTrench® MOSFET

30V, **5.0A**, **40m** Ω

General Description

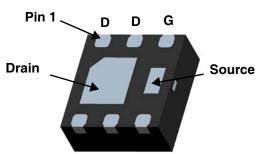
This Single N-Channel MOSFET has been designed using Fairchild Semiconductor's advanced Power Trench process to optimize the $\rm R_{DS}(on)$ @V_GS=2.5V on special MicroFET leadframe.

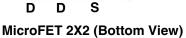
Applications

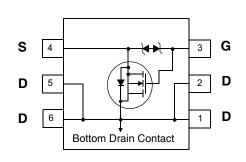
■ Li-Ion Battery Pack

Features

- $R_{DS(on)} = 40 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{ V}$, $I_D = 5.0 \text{A}$
- $R_{DS(on)} = 50 \text{m}\Omega$ @ $V_{GS} = 2.5 \text{ V}$, $I_D = 4.5 \text{A}$
- Low Profile-0.8mm maximum-in the new package MicroFET 2x2 mm
- HBM ESD protection level > 2.5kV typical (Note 3)
- Free from halogenated compounds and antimony oxides
- RoHS Compliant







Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V_{DSS}	Drain-Source Voltage	30	V	
V_{GSS}	Gate-Source Voltage		±12	V
ı	Drain Current -Continuous	(Note 1a)	5.0	۸
'D	-Pulsed		20	A
В	Power dissipation (Steady State)	(Note 1a)	2.4	w
P_{D}		(Note 1b)	0.9] vv
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient	(Note 1a)	52	°C/W
$R_{\theta,IA}$	Thermal Resistance, Junction-to-Ambient	(Note 1b)	145	*C/VV

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape Width	Quantity
430	FDMA430NZ	7"	8 mm	3000 units

Electrical Characteristics $T_J = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Characteristics						
B _{VDSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V$, $I_{D} = 250\mu A$	30			V
$\frac{\Delta B_{VDSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C		25.2		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V,$			1	μΑ
I _{GSS}	Gate-Body Leakage,	$V_{GS} = \pm 12V, \ V_{DS} = 0V$			±10	μΑ

On Characteristics (Note 2)

V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.6	0.81	1.5	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C		-3.2		mV/°C
		$V_{GS} = 4.5V, I_D = 5.0A$		23.6	40	
		$V_{GS} = 4.0V, I_D = 5.0A$		23.9	41	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = 3.1V, I_{D} = 4.5A$		25.4	43	
DS(ON)	Statio Brain Goardo On Flediciano	$V_{GS} = 2.5V, I_{D} = 4.5A$		27.6	50	11132
		$V_{GS} = 4.5V, I_D = 5.0A,$ $T_J = 150$ °C		37.0	61	
9 _{FS}	Forward Transconductance	$V_{DS} = 5V, I_{D} = 5.0A$		25.6		S

Dynamic Characteristics

C _{iss}	Input Capacitance	$V_{DS} = 10V, V_{GS} = 0V,$	600	800	pF
C _{oss}	Output Capacitance	f = 1.0MHz	110	150	pF
C _{rss}	Reverse Transfer Capacitance		75	115	pF
R_G	Gate Resistance	f = 1.0MHz	3.5		Ω

Switching Characteristics (Note 2)

t _{d(on)}	Turn-On Delay Time		8.3	17	ns
t _r	Turn-On Rise Time	$V_{DD} = 10V$, $I_D = 1A$	7.1	15	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 4.5V$, $R_{GEN} = 6\Omega$	18.1	37	ns
t _f	Turn-Off Fall Time		6.0	12	ns
Q_g	Total Gate Charge	101/ 1 5 04	7.3	11	nC
Q_{gs}	Gate-Source Charge $V_{DS} = 10V, I_D = 5.0A, V_{GS} = 4.5V$		0.8	2	nC
Q_{gd}	Gate-Drain Charge	VGS - 4.5 V	1.9	3	nC

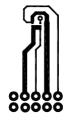
Drain-Source Diode Characteristics and Maximum Ratings

I _S	Maximum Continuous Drain-Source Diode Forward Current			2.0	Α
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 2.0A$	0.69	1.2	V
t _{rr}	Diode Reverse Recovery Time	$I_F = 5.0A,$		17	ns
Q _{rr}	Diode Reverse Recovery Charge	di/dt = 100A/μs		5	nC

Notes: 1. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.



a. 52 °C/W when mounted on a 1 in² pad of 2 oz copper.



b. 145 °C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < $300~\mu s$, Duty Cycle < 2.0%3. The diode connected between the gate and the source serves only as proection against ESD. No gate overvoltage rating is implied.

Typical Characteristics T_J = 25°C unless otherwise noted

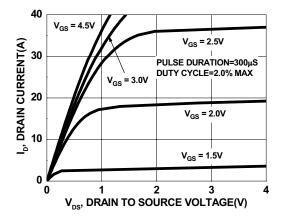


Figure 1. On Region Characteristics

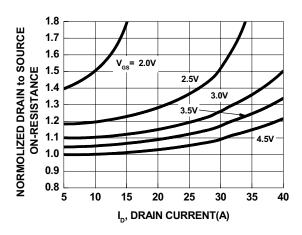


Figure 2. On-Resistance vs Drain Current and Gate Voltage

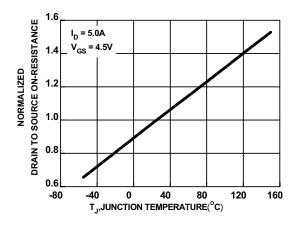


Figure 3. Normalized On Resistance vs Junction Temperature

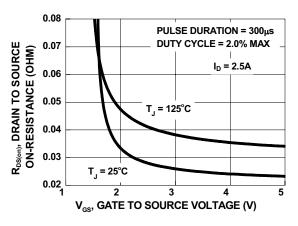


Figure 4. On-Resistance vs Gate to Source Votlage

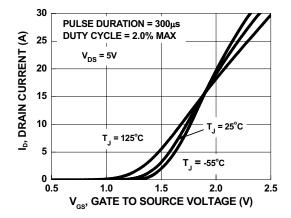


Figure 5. Transfer Characteristics

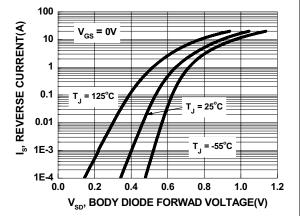
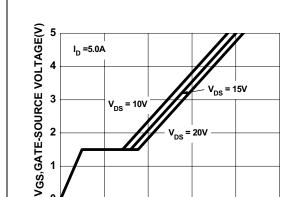


Figure 6. Source to Drain Diode Forward Voltage vs Source Current



Typical Characteristics T_J = 25°C unless otherwise noted

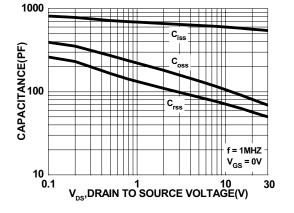
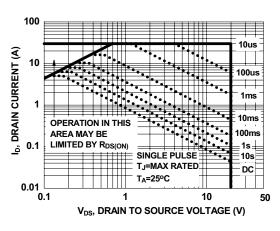


Figure 7. Gate Charge Characteristics

 $\begin{array}{c} 4 & 6 \\ \text{Qg,GATE CHARGE (nC)} \end{array}$

Figure 8. Capacitance vs Drain to Source Voltage



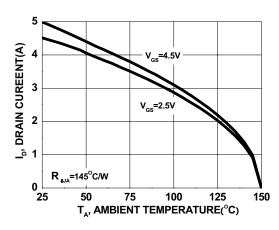
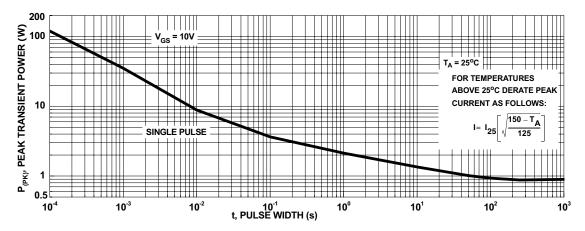


Figure 9. Safe Operating Area

Figure 10. Maximum Continuous Drain Current vs
Ambient Temperature



10

Figure 11. Single Pulse Maximum Power Dissipation

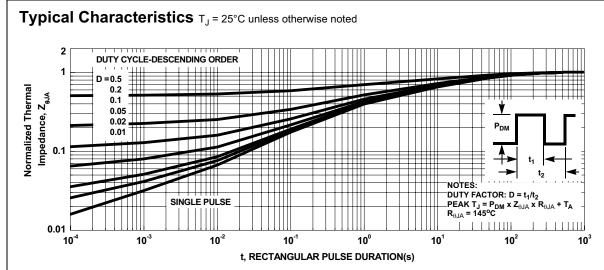
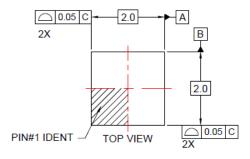
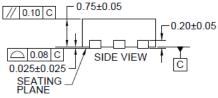
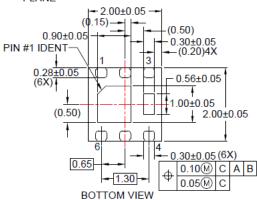


Figure 12. Transient Thermal Response Curve

Dimensional Outline and Pad Layout

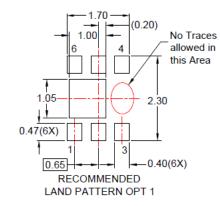


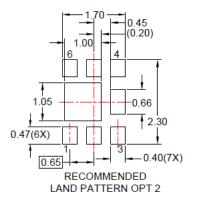




NOTES:

- A. PACKAGE DOES NOT FULLY CONFORM TO JEDEC MO-229 REGISTRATION
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.
- E. DRAWING FILENAME: MKT-MLP06Lrev4.







Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/package/packageDetails.html?id=PN_MLDEB-C06





TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

F-PFS™ FRFET® AX-CAP BitSiC™ Global Power ResourceSM Build it Now™

GreenBridge™ Green FPS™ CorePLUS™ Green FPS™ e-Series™ CorePOWER™ $CROSSVOLT^{rm}$ Gmax™

GTO™ $\mathsf{CTL}^{\mathsf{TM}}$ Current Transfer Logic™ IntelliMAX™ DEUXPEED® ISOPLANAR™

Dual Cool™ Marking Small Speakers Sound Louder EcoSPARK® and Better™

EfficentMax™ MegaBuck™ **ESBC™** MIČROCOUPLER™ MicroFET™ MicroPak™ Fairchild®

MicroPak2™ MillerDrive™ Fairchild Semiconductor® MotionMax™ FACT Quiet Series™ mWSaver® OptoHiT™ **OPTOLOGIC®**

(1)_® PowerTrench® PowerXS™

Programmable Active Droop™

QS™ Quiet Series™ RapidConfigure™ тм

Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™

SMART START™

Solutions for Your Success $^{\mathsf{TM}}$ SPM $^{\textcircled{\$}}$

STEALTH™ SuperFET® SuperSOT™-3 SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM ®* TinyBoost[®] TinyBuck[®] TinyCalc™ TinyLogic[®]
TINYOPTO™ TinyPower™ TinyPWM™ TinyWire™ TranSiC™ TriFault Detect™ TRUECURRENT®* μSerDes™

UHC® Ultra FRFET™ UniFET™ VCX^{TM} VisualMax™ VoltagePlus™ XS™ 仙童™

*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

OPTOPLANAR®

FACT[®] FAST[®]

FastvCore™

FETBench™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.
		Day ISO

Rev. 168

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor and see no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and h

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative